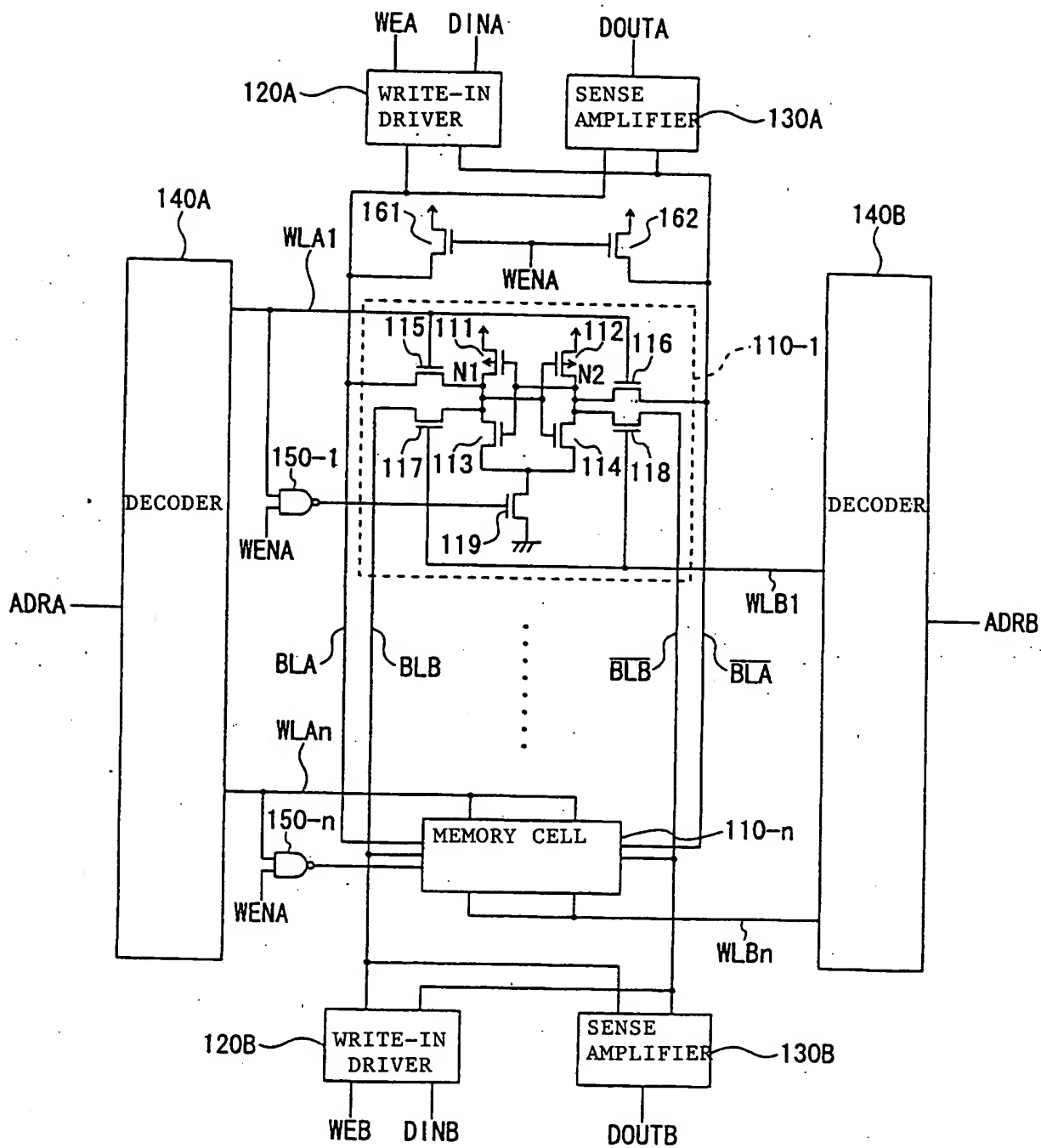


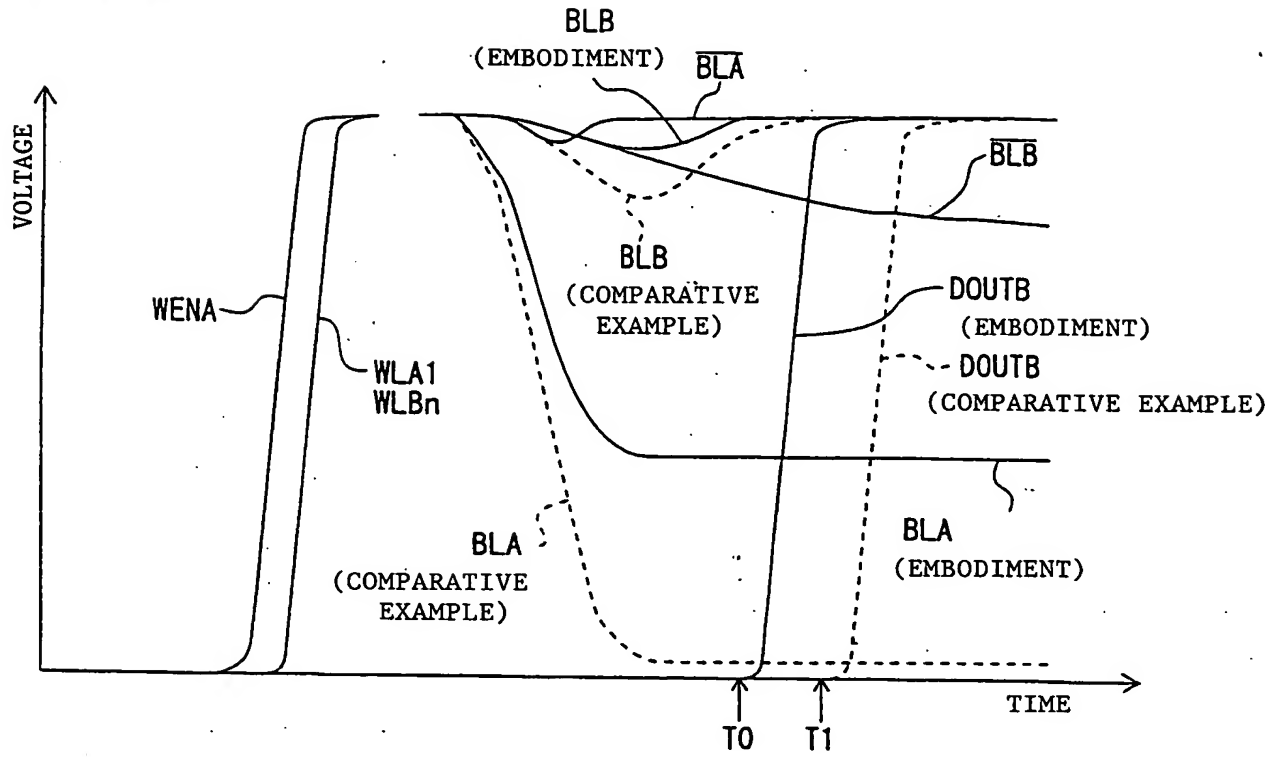
FIG. 1

100

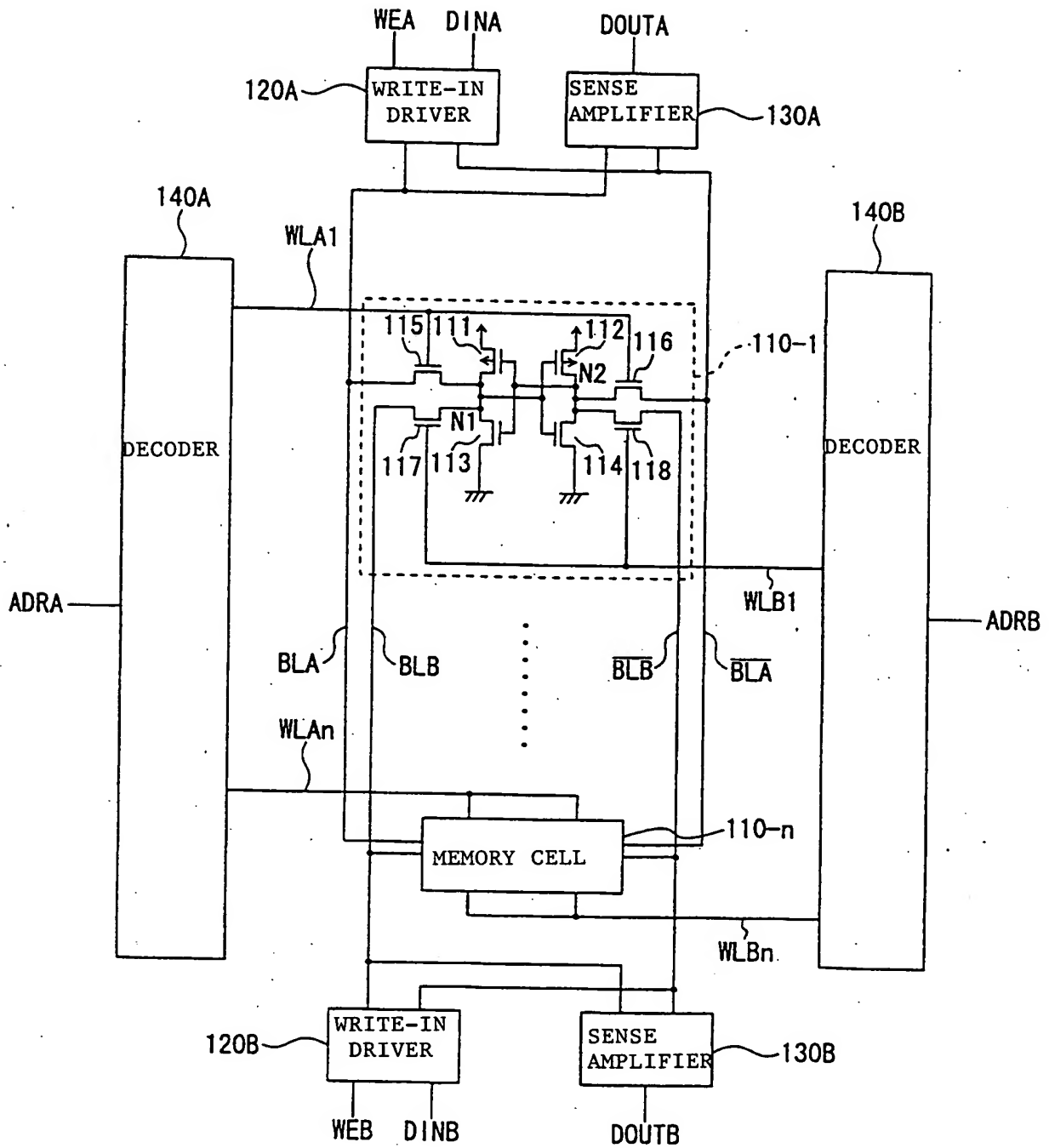


STRUCTURE OF THE FIRST EMBODIMENT

FIG. 2



OPERATION OF THE FIRST EMBODIMENT

FIG. 3300

STRUCTURE OF MULTI-PORT SEMICONDUCTOR
MEMORY FOR COMPARISON

